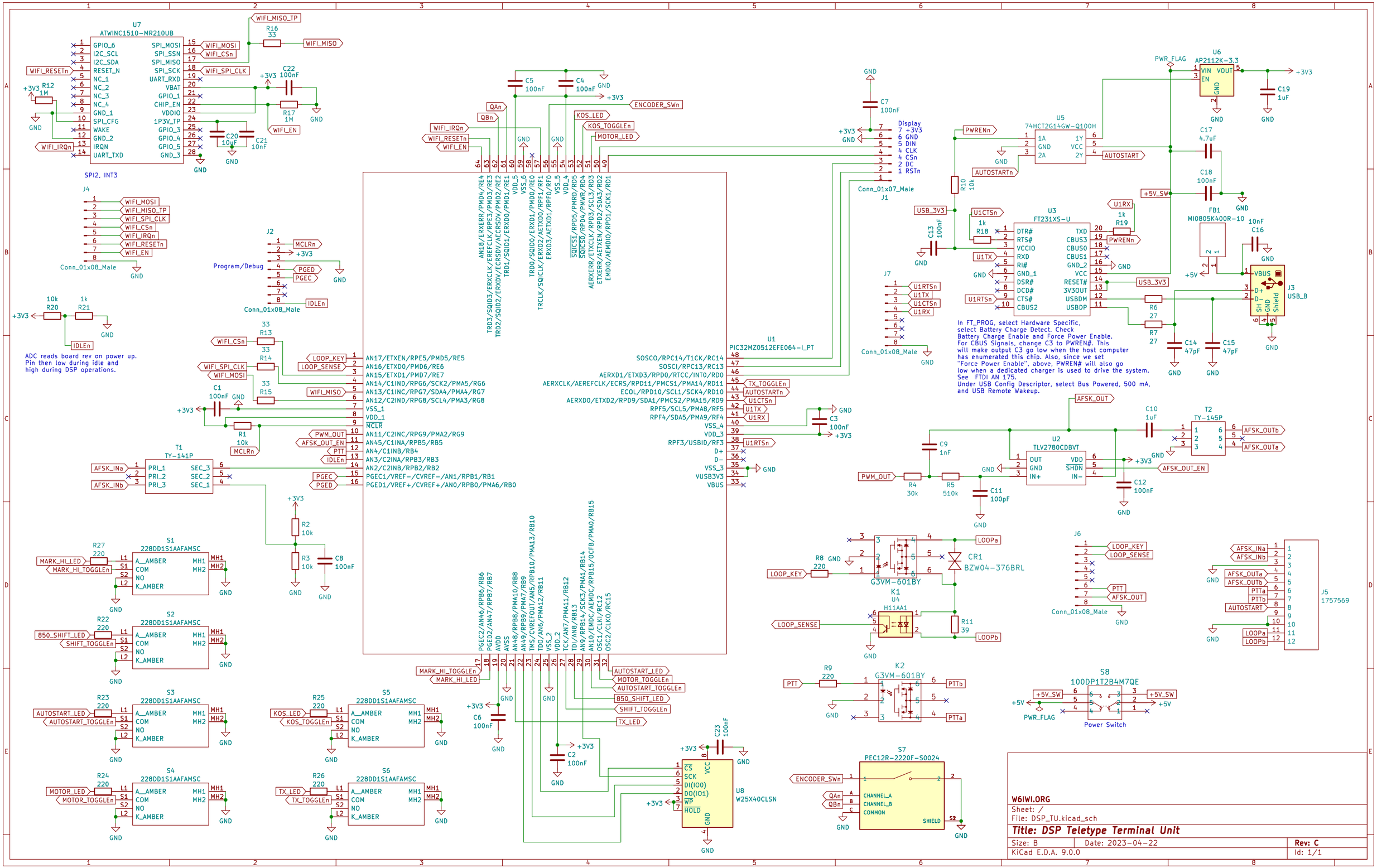
U7
ATWINC1510-MR210UB
U1
PIC32MZ0512EFE064-I_PT
U6
AP2112K-3.3
U5
74HCT2G14GW-Q100H
U3
FT231XS-U
U2
TLV2780CDBVT
T2
TY-145P
T1
TY-141P
J1
Conn_01x07_Male
J4
Conn_01x08_Male
J2
Program/Debug
J7
J6
J5
1757569
J3
USB_B
FB1
MI0805K400R-10
S1
S2
S3
S4
S5
S6
S7
PEC12R-2220F-S0024
S8
100DP1T2B4M7QE
Power Switch
U8
W25X40CLSN
K1
G3VM-601BY
K2
G3VM-601BY
U4
H11AA1
CR1
BZW04-376BRL
ADC reads board rev on power up.
Pin then low during idle and
high during DSP operations.
In FT_PROG, select Hardware Specific,
select Battery Charge Detect. Check
Battery Charge Enable and Force Power Enable.
For CBUS Signals, change C3 to PWREN#. This
will make output C3 go low when the host computer
has enumerated this chip. Also, since we set
"Force Power Enable", above, PWREN# will also go
low when a dedicated charger is used to drive the system.
See FTDI AN 175.
Under USB Config Descriptor, select Bus Powered, 500 mA,
and USB Remote Wakeup.
W6IWI.ORG
Sheet: /
File: DSP_TU.kicad_sch
Title: DSP Teletype Terminal Unit
Size: B
Date: 2023-04-22
Rev: C
KiCad E.D.A. 9.0.0
Id: 1/1